

AMENDMENTS TO THE CLAIMS

This listing of claims replaces all prior versions and listings of claims in the application.

Listing of Claims:

1-2 (canceled).

3 (currently amended). A pilot signal reception method, comprising:
receiving pilot signals of a pilot channel;
despreadering the received pilot signals;
measuring a variation in the reception intensity of a demodulated signal obtained by said despreadering; and
adaptively changing despreadering timings of the received pilot signals based on the measured variation in the reception intensity, in accordance with random reception timings.

4 (previously presented). The pilot signal reception method according to claim 3, wherein the despreadering timings are adaptively determined based on the

measured variation in the reception intensity in such a way as to avoid valleys of the variation in the intensity of the reception signal.

5 (previously presented). The pilot signal reception method according to claim 3, wherein the despreading timings and despreading period are adaptively changed based on the measured variation in the reception intensity in such a way as to avoid valleys of the variation in the intensity of the reception signal.

6-10 (canceled).

11 (currently amended). A receiver that receives pilot signals, comprising:
a despreading circuit that despreads the received pilot signals; and
a timing control signal generation circuit that generates a timing control signal to make despreading timings of the received pilot signals ~~irregular~~ random,
wherein said timing control signal generation circuit generates a timing control signal by detecting the intensity of a demodulated signal output from said despreading circuit and adaptively determining despreading timings in such a way as to avoid valleys of a variation in the intensity of the reception signal based on time variation of the detected intensity.

12 (previously presented). A receiver that receives pilot signals, comprising:

- a despreading circuit that despreads the received pilot signals;
- a timing determination circuit that determines the start timing of said despreading based on the intensity and variation of the intensity of a demodulated signal output from said despreading circuit; and
- a despreading chip number determination circuit that determines the number of chips to be despread based on the intensity and variation of the intensity of the demodulated signal output from said despreading circuit,

wherein operation of said despreading circuit is controlled based on the determined despreading timing and despreading chip number in such a way as to despread pilot signals adaptively and irregularly.

13-14 (canceled).